

METHOD AND SYSTEM FOR EXCHANGING DATA

Background of the Invention:

Field of the Invention:

5 The invention relates to a method and a system for exchanging data between a program-controlled device, in particular a microcontroller, and a logic circuit.

Summary of the Invention:

It is accordingly an object of the invention to provide a method and system for exchanging data that overcomes the hereinafore-mentioned disadvantages of the heretofore-known devices of this general type and that minimize the required lines and connections.

With the foregoing and other objects in view, there is
15 provided, in accordance with the invention, a method for exchanging data between a program-controlled device and a logic circuit. The first step of the method is providing a first line connecting the program-controlled device and the logic circuit. The next step is providing a bidirectionally
20 usable second line connecting the program-controlled device and the logic circuit. The next step is transmitting a control signal from the program-controlled device to the logic

circuit over the first line. The next step is placing the second line in a dominant state with the logic circuit when the logic circuit is to make a data transmission.

With the objects of the invention in view, there is also
5 provided a system for exchanging data. The system includes a program-controlled device, a logic circuit, a first line, and a second line. The first line conducts a control signal from the program-controlled device to the logic circuit. The bidirectionally usable, second line connects the program-controlled device to the logic circuit. The logic circuit can place the second line in a dominant state when data transmission is to be made by the logic circuit.

When a program-controlled device, in particular a microcontroller, is connected to a logic circuit, for example a PLD circuit, which also operates in accordance with a
15 program, data is exchanged in both directions, and simultaneously data is transmitted at the request of the logic circuit.

This object is achieved with the method according to the
20 invention in that a control signal (i.e., a strobe) is transmitted from the program-controlled device to the logic circuit along a first line, and in that at least one bidirectionally usable, second line is placed in the dominant

state by the logic circuit if a data transmission is to be made by the logic circuit.

The method according to the invention can be applied particularly advantageously if the logic circuit itself requests a data transmission. This is the case in particular if the logic circuit receives input variables independently of the microcontroller. This is the case for example with a radio locking circuit for motor vehicles in which the radio receiver and the logic circuit form one unit. The expenditure in terms of lines and connections is particularly low when the method according to the invention is applied, if, for the bidirectional transmission of data, only an individual, second line is used for the serial transmission. However, the invention does not rule out two or more (or, if appropriate seven or eight) second lines being provided. When including multiple second lines, correspondingly large numbers of bits can be transmitted in parallel.

A data transmission to the logic circuit by the program-controlled device prevents collisions caused by simultaneous transmission in both directions. Because a request to transmit data is made by the program-controlled device, the data transmission to the logic circuit is enabled if the second line is recessive for a predefined time after a level change of the control signal. After the predefined time has

expired, the second line is switched to the dominant state by the program-controlled device. As a result thereof, following a level change of the control signal, data transmission from the program-controlled device to the logic circuit is started.

- 5 With respect to the data transmission from the logic circuit to the program-controlled device, a collision is avoided by switching the second line to the recessive state with the program-controlled device before the predefined time has expired. This switching allows data transmission from the logic circuit to the program-controlled device.

Measures for receiving data by the program-controlled device include, maintaining the second line in the recessive state and allowing the control signal to assume the first level, after which the predefined time is started. The second line maintains the recessive state when the program-controlled device receives a request to receive. Furthermore, the second line remains recessive once it is in that state. (Check Translation)

In accordance with a further object of the invention, a control signal (strobe) can be transmitted from the program-controlled device to the logic circuit by a first line. Furthermore, a bidirectionally usable second line that connects the program-controlled device to the logic circuit

can be placed in the dominant state by the logic circuit if the logic circuit is to transmit data.

Other features which are considered as characteristic for the invention are set forth in the appended claims.

5 Although the invention is illustrated and described herein as embodied in method and system for exchanging data, it is nevertheless not intended to be limited to the details shown, since various modifications and structural changes may be made therein without departing from the spirit of the invention and within the scope and range of equivalents of the claims.

10 The construction and method of operation of the invention, however, together with additional objects and advantages thereof will be best understood from the following description of specific embodiments when read in connection with the accompanying drawings.

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Brief Description of the Drawings:

Fig. 1 is a block circuit diagram of an system; and

Fig. 2 is a state diagram of the system during the method;

Fig. 3 is a state diagram of the system during the method;

Fig. 4 is a state diagram of the system during the method;

Fig. 5 is a state diagram of the system during the method; and

Fig. 6 is a state diagram of the system during the method.

Description of the Preferred Embodiments:

5 The term component is used below to refer to the microcontroller and to the logic circuit. The entirety of the two components including the programs running in both components will be referred to below as a system.

Referring now to the figures of the drawings in detail and first, particularly to Fig. 1 thereof, there is shown a microcontroller that can be part of a control unit for various purposes. One such purpose is in the electronic system of a motor vehicle. Some of the functions of the overall system are performed by a logic circuit 2, which is embodied, for example, as a PLD (Programmable Logic Device). The microcontroller (μ C) 1 operates as what is referred to as a Master, while the logic circuit 2 not only operates as what is referred to as a Slave but also in certain cases engages actively in the exchange of data and signals a transmission of data. This may be necessary, for example, if the logic circuit 2 also receives signals from other devices. Such a

function is provided, for example, in a radio receiver for a central locking system of a motor vehicle.

The microcontroller 1 and the logic circuit 2 are connected via a first line 3 that is operated unidirectionally and conducts a control signal that is designated below as strobe. At least one further line 4 is used for bidirectional transmission of data. Depending on the particular request, a plurality of lines 4, for example two, four or eight, may also be provided in parallel.

Fig. 2 shows a state diagram, namely two states 5, 6 in which both the microcontroller 1 and the logic circuit 2 may be. In the first state, the transmission mode is determined and, in the second state 6, the data transmission takes place. A changeover from state 5 into the state 6 takes place as a result of an event in the program run of one of the two components that determines a transmission mode, for example the transmission of data from the microcontroller to the logic circuit. As a response or as an activity responding to this event, the system goes into the data transmission state. If a message occurs in this state, for example as a result of an end sign of the transmitted data - indicating that the data transmission is terminated - the system goes back into the state 5.

The state diagram according to Fig. 3 explains the determination of the transmission mode 5' by the microcontroller 1 in an illustration that is more detailed than Fig. 2. Within the state 5, the microcontroller can assume a sleep state 7 and a transmit state 8, a request state 9, and a receive state 10. The microcontroller goes into the state 5' at the event E "Data transmission terminated" or "Reset" after switching on (Power-on Reset).

The microcontroller goes into the transmit state 8 from the Sleep state 7 as a result of the events in which the program requests a transmission (Transmit) in the microcontroller and the data line 4 (Fig. 1) is switched to the recessive state. The data line then remains in this state while the strobe line assumes a first state (Valid) and a timer T is started. The timer T is used to give the logic circuit priority during the data transmission over the transmission of data by the microcontroller. This avoids collisions caused by both components transmitting at the same time. To this extent, the programming possibilities of delaying a transmission process once it has been prepared are not provided as they are in a microcontroller.

If the transmit state 8 is reached, the further activities A depend on whether the timer T has expired and whether the data

line is switched to the recessive or dominant state.

Switching to the dominant state means at this point that the logic circuit has transmitted a request. For this reason, the data line is kept in the recessive state by the

5 microcontroller while the strobe assumes the invalid state so that a "Receive" data transmission (viewed in terms of the state of the microcontroller) can begin. However, if the data line is recessive, it is switched to the dominant state and the strobe is switched to invalid. In this way, the
10 microcontroller can start to transmit data.

If the microcontroller is already in the sleep state 7 and the data line is dominant, the Request state 9 is reached by virtue of the fact that the data line is set to the recessive state, from the point of view of the microcontroller, and the
15 strobe is set to valid. The timer T is also started. After the timer T has expired and if the data line is dominant, that is to say has been switched to dominant by the logic circuit, the same state "Receive" is reached again from this state. If the program in the microcontroller requests the reception of
20 data and if the data line is recessive, the system goes from the Sleep state 7 into the "Receive" state 10, after which the "Receive" state is reached again after the timer has expired and the data line is recessive again.

Fig. 4 shows the determination of the transmission mode 5'' in the logic circuit. A sleep state 14 again is the start. From this state, the data line is switched to the recessive state if there is no request and the strobe signal is valid. In this way, a state 15 is reached which signifies "Receive or Transmit" for the logic circuit, in each case at the initiative of the microcontroller. From this state, when the strobe is invalid and the data is dominant, the data line is switched to the recessive state, after which the microcontroller can transmit and the logic circuit can receive. However, if the data line is recessive, a data transmission from the logic circuit to the microcontroller can take place.

The sleep state 14 can also be exited if there is a request for transmission of data from the logic circuit and the strobe signal is set to invalid. The data line is then set to the dominant state by the logic circuit and a state 16 request I is reached. If the strobe is then set to valid (by the microcontroller), the data line remains in the dominant state. A further state 17 request II is reached, from which the data line is set to the recessive state after the strobe has changed to invalid, and the data transmission from the logic circuit to the microcontroller can start (Receive).

The data transmission of the microcontroller is illustrated in Fig. 5. After the microcontroller 1 and the logic circuit 2 have been prepared for the transmission of data according to Figs. 3 and 4, the state 6' and 6'' (Fig. 6) is reached in both components. When data is transmitted to the logic circuit, after the event in which the strobe is set to the level invalid, the data line is set to the transmitted date and the strobe is set to valid. In this way, the state 21 transmit I is reached, after which the strobe is set to invalid after the transmission of the one data item (a bit or a byte or another group of bits). In the state 22 transmit II it is then tested whether all the data has been transmitted. If this is not the case, the next data item is transmitted at 21, but if all the data has been transmitted, the data line is set to the recessive state after the state 22. A similar procedure is adopted in the microcontroller with the states 23, 24 for the reception of data from the logic circuit.

Fig. 6 shows the corresponding processes in the logic circuit for the transmission of data to the logic circuit (states 25, 26)--transmit I and transmit II and states 27 and 28 for the transmission in the direction to the microcontroller--receive I and receive II.